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L1 370/398.ccls.

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Integrated multiport **switch** having management information base (MIB) 180 may be configured to generate a **DMA request** to the memory controller 104.

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This allows any pending **DMA request** to gain control 7532 * of the local bus in line, size)) 7943 return -EFAULT; 7944 7945 **switch** (new_line.clock_type) ...

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This **overflow buffer** can be located on any. 4KB page boundary in SDRAM, automatically **switch** between buffers. • Maskable interrupt generation ...

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cause the remote power **switch** to shut off by asserting This **overflow buffer** can be located on any. 4KB page boundary in SDRAM, allowing software to ...

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This is a phenomenon whereby a manual **switch** repeatedly turns on and off in a short time because of vibration of the contact.

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DMA Request 0. A5. DACK0. DMA Acknowledge 0 ... This **overflow buffer** can. be located on any 4KB page boundary in SDRAM,. allowing software to set the MMU ...

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as an **overflow buffer**. When the allocated buffer space in the SDRAM is flag can be set by API as part of the channel **switch** initialization process. ...

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